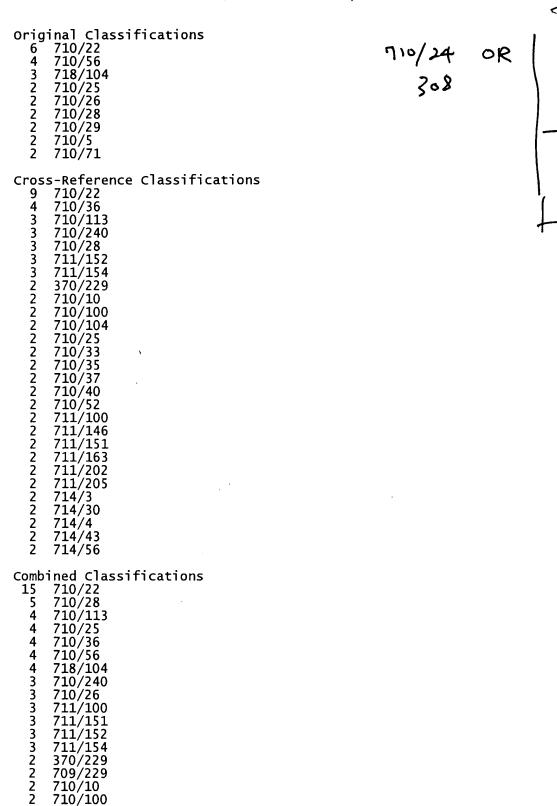
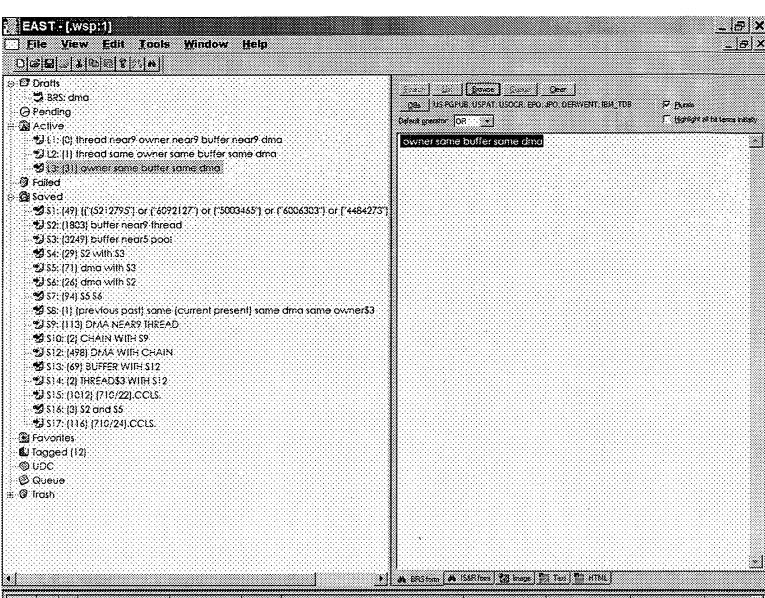
## 10682410\_CLS1.txt Most Frequently Occurring Classifications of Patents Returned From A Search of 10682410 on June 14, 2005

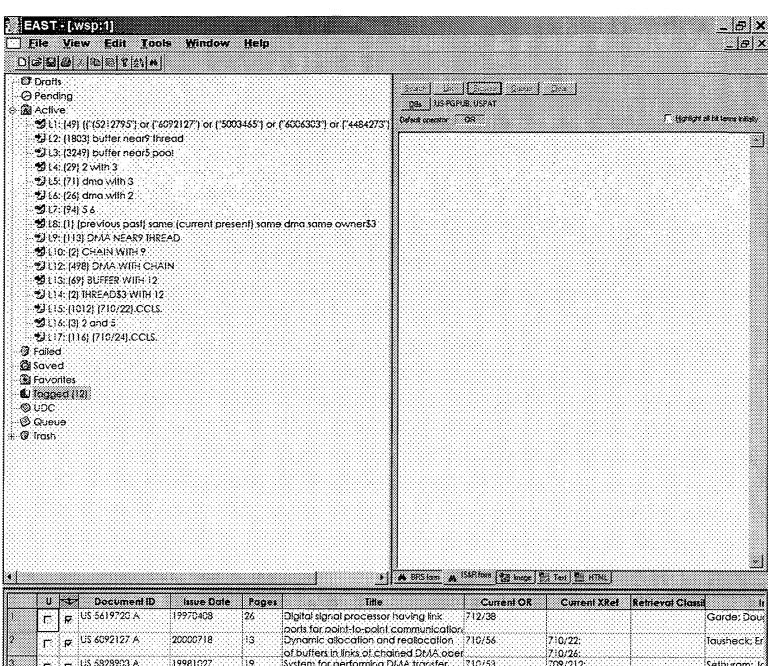


## 10682410\_CLS1.txt

2 2 2 2 2 2 2	710/104 710/108 710/200 710/244 710/29 710/33 710/35
222222222222222222222222222222222222222	710/37 710/40 710/5 710/52 710/71 710/8
2 2 2 2 2 2 2	711/142 711/146 711/163 711/202 711/205 714/3
2 2 2 2 2	714/30 714/4 714/43 714/56



	u		Document ID	Issue Date	Pages	Title	Current OR	Current XRef	Retrieval Class	i ·
1	г	-	US 20050188129 A1	20050825	35	Facilitating Inter-DSP data	710/52	711/100		Abdellah
						communications.				1
2	-	г	US 20050080943 AT	20050414		Method and apparatus for efficient	710/22			Pafumi, Ja
					ata da	sharing of DMA resource			e }e je	
3	г	г	US 20040148382 A1	20040729	126	Language for handling network	709/223			Narad, C
						packets				
4	г	г	US 20040143655 A1	20040722	91	`#````````````````````````````````````	709/223			Narad, C
						protocol (ICP) seaments	4			
3	г	П	US 20040073738 A1	20040415	15	Scalable efficient I/O port protocol	710/308	[[7]][ <b>7]]47</b> ]		Kessler, Ri
					4					
5	Γ.	г	US 20040042483 A I	20040304	28	System and method for ICP attload	370/463	370/419		Elzur, Url €
		Г	US 20040042458 A.I	20040304	28	'Biratain'a ni alaisian analainini analainin alain ni alaininin alainin ni ∰ininininin	370/394	370/389		Ezu, Uri
						out-of-order frames				
•	Π.	г	US 20030061332 A1	20030327	.ó₹	Muitiple consumer-multiple producer	709/223			Narad, Cl
						rinas				
		T.	U\$ 20030046423 A1	20030306	90	Programmable system for processing a	7097238			Narad Ci
			11. Pananaan 11. A.	00000100		partitioned network intrastructure	7001000			1
0	Г	Г	US 20030005103 AT	20030102	93	Cumulative status of arithmetic	709/223	709/201		Norad Ci
			D. C. 000000000000 6.4	00000101	100	operations	indigental de la companya del companya del companya de la companya			
		τ.	US 20020013886 A1	20020131	18	Multiprocessor system	711/130	711/147		Higuchi, T
•			(·c (0000070 D)	20050503		Method and apparatus for fast	710/266	710/261:		∺oerler; J
2	Г	Г	US 6887278 B1	20030303	17	di terreta de la compositación	7107.200			Hoerier, J
3			US 6859841 82	20050222	83	acknowledgement and efficient servi. Programmable system for processing a	700 1004	710/267		ai. Narad: Ci
•	Г.	П	U3 6637641 02	20000222	03	`````` <del>```````````````````````````````</del>	7077230.			Nuitad. Ci
4			US 6738836 E1	20040518	16	partitioned network infrastructure Scalable efficient I/O port protocol	710/22	710/105:		Kessler: Ri
	Г	ि	C3 0/ 30030 D :	200700)6		acondes emelecuit o port projects		718/107:		KOME, KI
5			US 6701338 82	20040302	88	Cumulative status of arithmetic	708/525	709/223		Narad: C
•	T.	Γ	93-03-03-03-03-03-03-03-03-03-03-03-03-03	20070502		operations	14, <b>06</b> 4,020	7077225		. varou, C
14			3:5 4405480 BO	Sangages	QΟ	Atuiticle consumer multiple producer	711 (316			Narad C
<b>*</b> 1000										• •



U	7	Document ID	issue Dale	Pages	Title	Current OR	Current XRef	Retrieval Class	ii I
П	Ş	US 5619720 A	19970408	26	Digital signal processor having link	712/38			Garde; Dou
<b>]</b>					ports for point-to-point communication				
٦٣.	₽	US 6092127 A	20000718	13	Dynamic allocation and reallocation	710/56	7 0/22;		Tausheck; Et
		ૄૺૺૺૺૺૺૺૺૺૺૺૺૺૺૺૺૺૺૺૺૺૺૺૺૺૺૺૺૺૺૺૺૺૺૺૺ	. ] . ] . ] . ] ] . ] . ] . ] . ]		of buffers in links of chained DtAA oper	to the standard and a standard and a standard and a	7:0/26:		
1	₽	US 5828903 A	19981027	19	System for performing DMA transfer	710/53	709/212;		Sethuram: Jo
	ļ	4			with a pipeline control switching such	1-1-1-1-1-1-1-1-1-1-1-1-1-1-1-1-1-1-1-	7 18/22		
l r	D	US 6324599 B1	20011127	jn:	Computer system and method for	710/26	710/22;		Zhou; Ning e
3					tracking DMA transferred data within		718/24:		
г	₽	US 6324598 81	20011127	12	Software enlarged tag register and	710/24	710/22.		Oson; Sleve
				10000	method thereof for noting the complet		710/62		
٣	ø	US 5850570 A	17781215	13	Communication apparatus for	710/24	710/17;		Shoji; Fumio
					performing data transfer utilizing direct		710/18:		
г	5	US 5933654 A	19990803	14		710/23	710/22		Galduni Dar
					controller				
	D	US 20050033874 A1	20050210	m	Direct memory access using memory:	710/22			Futral, William
		1			descriptor iist				
Г	5	US 6718405 82	20040406	9	Hardware chain pull	710/22	710/24:		Rogers: Jeffi
							710/308:		
~	7	US 5781799 A	19980714	16	DMA controller arrangement having	710/22			Leger: Gear
					alurality of DMA controllers and buffer				
г	5	US 20040267982 AT	20041230	17	Read/write command buffer pool	710/52			Jackson. The
		L			resource management using read-pat				
-		US 6336150 B1	20020101		Apparatus and method for enhancing	710/5	710/18:		Elis; Jacksor
	110				data transfer rates using transfer contr		710/33:		